



IP.com  
PriorArtDatabase

February 22, 2006

USPTO

Securing innovation

## Search

Full Text

Concept

Document ID

Recent Disclosures

Other

Prior Art Home

Support

Logout

Displaying records # 1 through 2 out of 2

Result # 1      Relevance: ○○○○○○

### Hardware Cycle Based Memory Residency

2003-05-22

IPCOM000012728D

English (United States)

Hardware Cycle Based Memory Residency implementation over traditional time based, or software based method addresses cache rehit probabilities and the overhead of scanning and freeing memory, and having pages stay resident in memory for long periods of time. This ...

Result # 2      Relevance: ○○○○

### A Kernel Model for Precision Timekeeping (RFC1589)

1994-03-01

IPCOM000002423D

English (United States)

This memorandum describes a model and programming interface for generic operating system software that manages the system clock and timer functions. The model provides improved accuracy and stability for most workstations and servers using the Network Time Protocol (NTP) or ...

Displaying page 1 of 1    << FIRST | < BACK | NEXT > | LAST >>

Search query: (hardware w/1 counter~) and threshold

[New search](#) | [Modify this search](#) | [Search within current results](#)

Copyright © 2006 IP.com, Inc. All rights reserved. | [Privacy Statement](#)